Javier A. Ortiz

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EDUCATION

University of Puerto Rico at Mayagüez, Puerto Rico

B.S. in Electrical Engineering / Major: Applied Electromagnetics

December 2013

EXPERIENCE

Advanced Radar Research Center University of Oklahoma

January 2015 – Present

Pursuing a doctoral degree in Electrical Engineering focused on antennas and active phased arrays.

May 2015 - August 2015

• NASA Goddard Space Flight Center

Student Intern

Graduate student internship modeling and assembling arbitrary waveform generator for D3R radar and developing a low-cost doppler development for an X-band, off-the-grid radar.

• Teaching Assistant Applied Electromagnetics Laboratory, UPRM

August 2013 - Present

Offered Advanced Design System seminars to students for modeling, designing, and simulating microstrip components and transmission lines. Aided students with the characterization and measurements of parameters for transmission lines with the use of Agilent's Network Analyzer and other RF equipment.

• IAP Undergraduate Research, University of Puerto Rico at Mayagüez

August2013 - Present

Antennas for Body Area Networks

Study of the effects of the human body on the performance of the antenna, in particular, the effects on the antenna input impedance. Study is done in Ansoft HFSS and in Remcom's XFDTD.

• IEEE Electromagnetics Council (AP-S, MTT-S, GRS-S) Joint Chapter

August 2012 - Present

President & Co-Founder

Put together an executive team to effectively organize activities such as design projects, workshops, seminars, and field trips related to applications in Antennas, Microwave Theory and Techniques, and Remote Sensing, to further develop our student's personal and technical skills.

• Antenna Theory & Design Project, University of Puerto Rico at Mayagüez

January 2013 - May 2013

Designed, simulated, fabricated, and tested a high gain, high directive, and cost effective broadband Yagi-Uda antenna.

- IAP Undergraduate Research & Capstone, University of Puerto Rico at Mayagüez August 2011 December 2012 Improvements to Load-Pull Bench for the Characterization of Microwave Power Transistors

 Implementation of an active load for the purpose of characterizing microwave power transistors, and development of a load-pull measurement bench based on the implemented active load and a varactor-tuned input matching network.
- GE Industrial Solutions, Humacao Puerto Rico

January 2012 - June 2012

Contacts Process Improvement and Productivity Engineer (COOP)

Involved in a manufacturing environment to implement process changes, which would provide benefits in labor and in materials through enrollment in productivity projects. Successfully overhauled, validated, and implemented solder chopper machine for benefits in material and labor in production.

ACHIEVEMENTS

UPRM Certificate in Project Management
 Second Place in The 8th Annual Industrial Engineering Research Competition
 Written Research Plan Proposal to the EPA for Technological Applications in Agriculture
 2010

SKILLS

- Project manager, leader, problem solver, hands on, planning, managing, team player, fast learner, adaptive worker.
- Fully Bilingual, Fluent in English and Spanish Languages, Basic German
- Non Curricular Hobbies: Reading, Computer Assembly, Vocalist, Drums and Percussion, Jogging, Mountain Biking
- Proficient in Personal Computer Skills (Windows, Macintosh, Linux, Unix, Advanced Design System, ANSYS Designer & HFSS, 4NEC2, Pspice, Matlab, AutoCAD, Visual Basic, Microsoft Office, Adobe Photoshop, Web Development, Network Analysis)